

Antonio Napolitano was born in Napoli, Italy, in 1964. He received the Ph.D. in Electronic Engineering and Computer Science in 1994, from the University of Napoli Federico II. In 1997, he was with the Department of Electrical and Computer Engineering at the University of California, Davis, USA, as a Postdoctorate Research Associate. Since 2005 he is Full Professor of Telecommunications at the University of Napoli Parthenope.

In 2005, he was Visiting Professor at the Institute de Recherche Mathematique de Rennes, Haute Bretagne, France. In 2010 and 2013 he was Visiting Professor at Laboratoire d'Analyse des Signaux & des Processus Industriels, Universite Jean-Monnet, Roanne, France. In 2014 and 2015 he was [Visiting Professor at Ecole Nationale Supérieure d'Electronique, d'Electrotechnique, d'Informatique et d'Hydraulique et Telecommunications \(INPT-ENSEEIH\)](#), Toulouse, France. He held visiting appointments at Universite de Nice--Sophia Antipolis, France; University of North Carolina, Chapel Hill, NC, USA; Centro de Investigacion en Matematicas, Guanajuato, Mexico; Econometric Department, Wyzsza Szkola Biznesu, Nowy Sacz, Poland; Institute of Mathematics, Krakow University of Technology, Krakow, Poland; School of Electrical and Information Engineering, University of South Australia.

In 1995 he received the [Best Paper of the Year Award from the European Association for Signal Processing \(EURASIP\)](#) for a paper on higher-order cyclostationarity. In 2007 was recipient of the [EURASIP Best Paper Award](#) for a paper on the functional approach in signal analysis. In 2008 he received from [Elsevier the Most Cited Paper Award](#) for a review article on cyclostationarity.

Since 2016 he is [IEEE Fellow](#). From 2006 to 2009 and from 2011 to 2015 he has been Associate Editor of the IEEE Transactions on Signal Processing. He is in the Editorial Board of Signal Processing (Elsevier) and Digital Signal Processing (Elsevier). He is reviewer for the American Mathematical Society and several IEEE and EURASIP journals. He was Technical Program Committee member in several international conferences. From 2008 to 2013 he has been Elected Member of the Signal Processing Theory and Method Technical Committee (SPTM-TC) of the IEEE Signal Processing Society. From 2017 he is Elected Member of the Sensor Array and Multichannel Technical Committee (SAM-TC) of the IEEE Signal Processing Society. He is EURASIP Local Liaison Officer.

He was coordinator and principal investigator of a [NATO grant on Signal Processing](#) and principal investigator of a [NATO grant on Communications Security](#).

He is the author of the book [Generalizations of Cyclostationary Signal Processing: Spectral Analysis and Applications](#), John Wiley & Sons Ltd. -- IEEE Press, 2012

Web page: <https://sites.google.com/site/antnapol>

Publications: <https://sites.google.com/site/antnapol/home/publications>